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Application Number:	10567171		
International Application Number:			
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Title of Invention:	CIRCUIT FOR SIGNAL AMPLIFICATION AND USE OF THE SAME IN ACTIVE MATRIX DEVICES		
First Named Inventor/Applicant Name:	Martin J. Edwards		
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1	Issue Fee Payment (PTO-85B)	GB030133U51_Issue_Fee_sign ed.pdf	115340	no	1
'	issue ree rayment (F10-03b)		d269d8258ee7c7fc1e9c23291b2db68f913 8598c		
Warnings:					

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Information:					
2	Fee Worksheet (PTO-875)	fee-info.pdf	32225	no	2
2 Fee Worksheet (F10-073)	· ·	66649934dd36654bf33c2603695ceecc12ccc e3	1		

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#### National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

## New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.